



IFW/  
PATENT

Attorney Docket N<sup>o</sup>: 01-490/1C  
Serial N<sup>o</sup>: 10/769,510

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Michael Eneboe, et al.  
Serial N<sup>o</sup> : 10/769,510  
Filed : January 30, 2004  
Group Art Unit : 2825  
Examiner : Tuyen P. To  
For : SYSTEM AND METHOD FOR OPTIMIZING AN INTEGRATED  
CIRCUIT DESIGN

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**TRANSMITTAL OF RESPONSE TO OFFICE ACTION  
AND TERMINAL DISCLAIMER**

---

**CERTIFICATE OF MAILING 37 C.F.R. § 1.8**

**MAILING**

☒ deposited with the United States Postal Service  
with sufficient postage as first class mail in an  
envelope addressed to the Commissioner for  
Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Penny L. Flint  
Penny L. Flint

**FACSIMILE**

☐ transmitted by facsimile to the Patent and  
Trademark Office.

DATED: February 10, 2006

Attached please find the following documents for the above referenced application:

1. *An Amendment and Response to the Office Action Dated November 17, 2005.*
2. *A Terminal Disclaimer* including authorization to charge fees to the deposit account.

Please address all correspondence to:

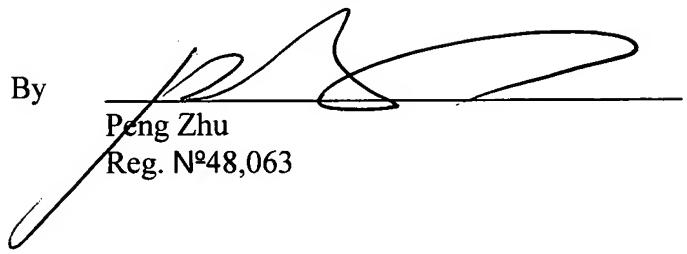
**CUSTOMER NO. 24319**  
PETER SCOTT  
LEGAL DEPARTMENT - IP  
LSI LOGIC CORPORATION  
M/S D-106  
1621 BARBER LANE  
MILPITAS, CA 95035

DATED: February 10, 2006.

Respectfully Submitted,

LSI Logic Corporation,

By



Peng Zhu  
Reg. N°48,063

SUITER • WEST • SWANTZ PC LLO  
14301 FNB PARKWAY, SUITE 220  
OMAHA NE 68154-5299  
(402) 496-0300 (TELEPHONE)  
(402) 496-0333 (TELECOPIER)